**Webinar: High-Performance Computing (HPC) – Looking back into the future**

October 31st, 2013 – Tuesday: 3:00 (GMT – 3hs: Brasília)

**Registration:** https://meetings.vtools.ieee.org/meeting_view/list_meeting/21361

Over the last 40 years High-Performance Computing (HPC) has established itself as the third pillar of modern Science, Research and Engineering, now also extending into Enterprise Business areas. Today HPC is vital and an essential part for new discoveries across a wide range of research areas and diverse applications. We will look back in the history of HPC, formerly called "Supercomputing", discuss its current status and the emerging trends and technologies going forward. We will also touch on one of the next big challenges and opportunities in HPC: ExaScale Computing. The future for performance is parallel.

**Presenter:**

Dr. Herbert Cornelius is Technical Director Advanced Computing at Intel EMEA. Before he was Engineering Manager in Intel's Cluster Software & Technologies group in EMEA, focusing on scalable parallel computing solutions based on vectorization/SIMD, multi-threading and message-passing for multi/many-core and multi-processor platforms.

Prior to this position he was the EMEA Technical Marketing Manager Enterprise Computing and New Technologies enabling. He joined Intel in 1993 as Senior Computational Scientist in the Scalable Systems Division and has held various technical and management positions in the areas of Applications and Software Engineering. Before joining Intel, he served as Manager High-End Computing Europe at Fujitsu and worked at Cray Research from 1983 to 1990. Prior he worked as Scientific Assistant for Applied Mathematics at the University of Karlsruhe. He received a Ph.D. degree in Mathematics and Diploma degree in Mathematics and Informatics from Technical University of Berlin, Germany.